

X.Org Developer's Conference 2019

Wednesday 02 October 2019

Main Track: Main track (07:55-17:40)

time	[id] title	presenter
08:30	[61] Opening Session	FILION, Mark
08:55	[18] Zink: OpenGL on Vulkan	FAYE-LUND, Erik
09:50	[38] Introducing the Vulkan WSI Layer	ZHELEV, Rosen Mr GARBETT, David Mr FRANCHIN, Matteo
10:20	[59] ACO, a new compiler backend for GCN GPUs	NIEUWENHUIZEN, Bas SCHÜRMAN, Daniel
11:05	Lunch	
12:40	[24] How to not write a back-end compiler	EKSTRAND, Jason
13:35	[34] Mediuimp support in Mesa	ROBERTS, Neil
14:20	Coffee Break	
14:50	[44] Implementing Optimizations In NIR	ROMANICK, Ian
15:45	[52] Linux Graphics CI: Standardizing the kernel CI workflow and hardware, and improving our testsuites	PERES, Martin
16:10	[17] Outreachy Internship Report: Refactoring backlight and spi helpers in drm/tinydrm	MADHYASTHA, Meghana
16:30	Coffee Break	
16:50	[64] Khronos Update & AMA	TREVETT, Neil

Thursday 03 October 2019

Main Track: Main Track (07:55-19:00)

time	[id] title	presenter
08:45	[63] Opening Session	FILION, Mark
09:00	[48] Monado: Open Source Augmented & Virtual Reality	HAAG, Christoph FERWERDA, Joey
09:55	[28] KWin now and tomorrow	GILG, Roman
10:15	Coffee Break	
10:45	[40] Improving frame timing accuracy in Mesa, DRM and X	PACKARD, Keith
11:40	[37] A case study on frame presentation from user space via KMS	FINK, Heinrich
12:25	Lunch	
14:00	[26] Nouveau Generic Allocator Implementation in GBM	JONES, James
14:55	[42] Bulk moving mechanism on LRU for DRM/TTM	HUANG, Ray
15:15	Coffee Break	
15:45	[39] Lima driver status update	NUNES, Erico KHORUZHICK, Vasily ABBOTT, Connor
16:40	[50] Freesync, Adaptive Sync & VRR	WENTLAND, Harry
17:35	[25] Enabling 8K displays - A story of 33M pixels, 2 CRTCs and no Tears!	NAVARE, Manasi
18:00	[65] X.Org Foundation Board of Directors Meeting	

Friday 04 October 2019

Main Track: Main Track (08:45-17:45)

time	[id] title	presenter
08:45	[66] Opening Session	FILION, Mark
09:00	[30] A whirlwind tour through the input stack development efforts	HUTTERER, Peter
09:55	[33] Edging closer to the hardware for kernel CI on input devices	TISSOIRES, Benjamin
10:15	Coffee Break	
10:45	[23] Witchcraft Secrets from a Reverse Engineer	ROSENZWEIG, Alyssa
11:40	[10] Everything Wrong With FPGAs	WIDAWSKY, Ben
12:25	Lunch	
14:00	[54] DRM/KMS for Android	DELVA, Alistair
14:25	[21] Display hardware testing with Chamelium	SER, Simon
14:50	[8] State of the X.org	VETTER, Daniel
15:10	Coffee Break	
17:25	[68] Closing session	STONE, Daniel